



## Description

### JMT N And P-Channel Enhancement Mode MOSFET

#### Features

- N-Channel: 30V 6A  
 $R_{DS(ON)}=18.6\text{m}\Omega$  (Typ.) @  $V_{GS}=10\text{V}$   
 $R_{DS(ON)}=30\text{m}\Omega$  (Typ.) @  $V_{GS}=4.5\text{V}$
- P-Channel: -30V -6A  
 $R_{DS(ON)}=27\text{m}\Omega$  (Typ.) @  $V_{GS}=-10\text{V}$   
 $R_{DS(ON)}=42\text{m}\Omega$  (Typ.) @  $V_{GS}=-4.5\text{V}$
- Excellent Gate Charge x  $R_{DS(ON)}$  Product(FOM)
- Very Low On-resistance  $R_{DS(ON)}$
- Fast Switching Speed

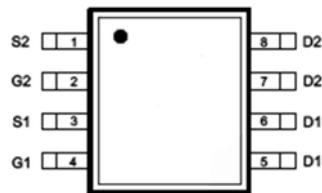
#### Application

- Battery Protection
- Load Switch
- Power Management

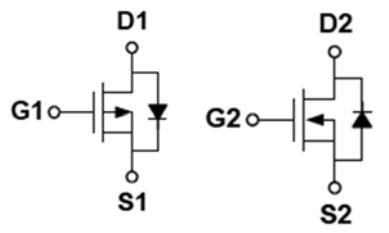
#### Package



JMTP4606A



Pin Assignment



P-Channel

N-Channel

#### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max. N-Channel	Max. P-Channel	Units
$V_{DSS}$	Drain-Source Voltage		30	-30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	6	-6	A
		$T_C = 100^\circ\text{C}$	5	-5	A
$I_{DM}$	Pulsed Drain Current <small>note1</small>		30	-30	A
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	2		W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		100		$^\circ\text{C}/\text{W}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150		$^\circ\text{C}$

**N-Channel Electrical Characteristics** ( $T_C=25^\circ C$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V, T_J=25^\circ C$	-	-	1.0	$\mu A$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.55	3.0	V
$R_{DS(on)}$ note2	Static Drain-Source on-Resistance	$V_{GS}=10V, I_D=6A$	-	18.6	22	$m\Omega$
		$V_{GS}=4.5V, I_D=5A$	-	30	37	$m\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=6A$	-	15	-	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0MHz$	-	255	310	pF
$C_{oss}$	Output Capacitance		-	45	60	pF
$C_{rss}$	Reverse Transfer Capacitance		-	35	50	pF
$Q_g$	Total Gate Charge	$V_{DS}=15V, I_D=6A, V_{GS}=10V$	-	5.2	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.0	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{GS}=10V, V_{DS}=15V, R_L=2.5\Omega, R_{REN}=3\Omega$	-	4.5	-	ns
$t_r$	Turn-on Rise Time		-	2.5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	14.5	-	ns
$t_f$	Turn-off Fall Time		-	3.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	6	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	30	A	

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$

**P-Channel Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

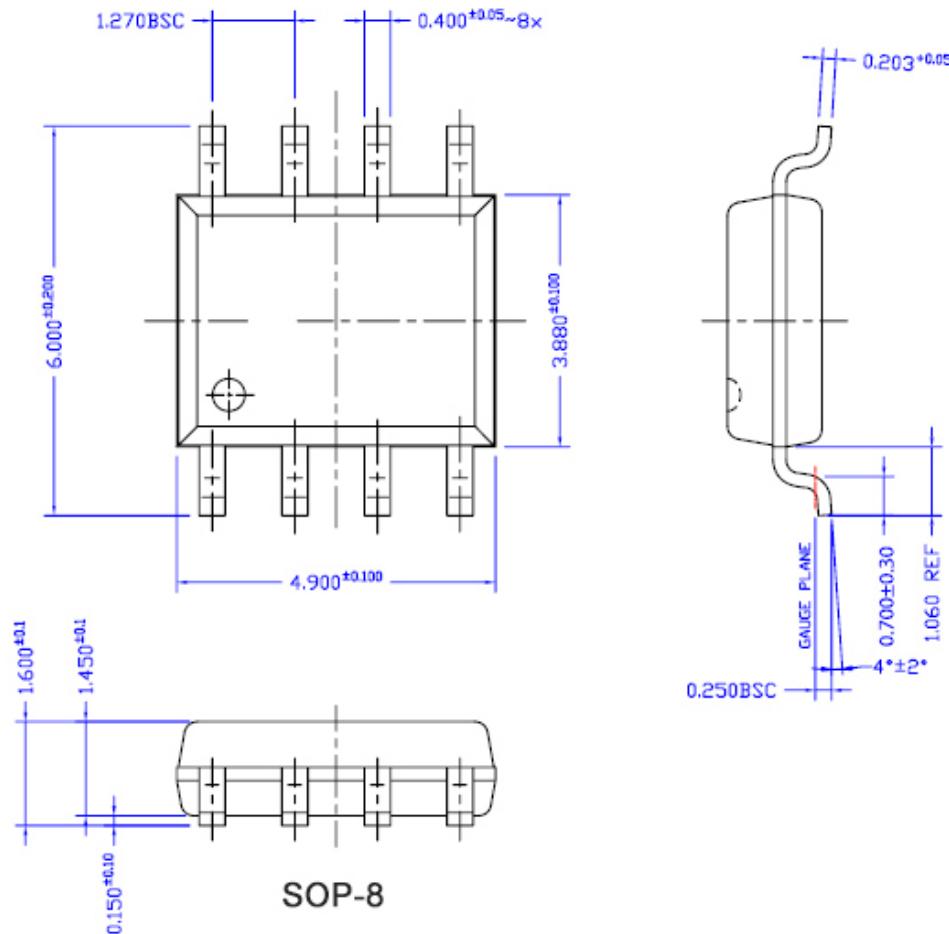
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D = -250\mu\text{A}$	-30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = -30V, V_{GS} = 0V,$	-	-	-1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.8	-1.32	-2.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS} = -10V, I_D = -6A$	-	27	35	$\text{m}\Omega$
		$V_{GS} = -4.5V, I_D = -5A$	-	42	50	
$g_{FS}$	Forward Transconductance	$V_{DS} = -5V, I_D = -6A$	-	18	-	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = -15V, V_{GS} = 0V, f = 1.0\text{MHz}$	-	760	-	pF
$C_{oss}$	Output Capacitance		-	140	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	95	-	pF
$Q_g$	Total Gate Charge	$V_{DS} = -15V, I_D = -6A, V_{GS} = -10V$	-	13.6	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	3.2	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS} = -15V, R_L = 2.3\Omega, R_{GEN} = 3\Omega, V_{GS} = -10V$	-	11	-	ns
$t_r$	Turn-on Rise Time		-	35	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	30	-	ns
$t_f$	Turn-off Fall Time		-	10	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-6	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-30	A	

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$



## Package Mechanical Data





Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the first version which is made in 27-Feb.-2019. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.  
Copyright ©2019 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.